IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor

:Naoto KIMURA

Filed

:Concurrently herewith

For

:SEMICONDUCTOR DEVICE.

Serial Number

:Concurrently herewith

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

April 19, 2004

## **INFORMATION DISCLOSURE STATEMENT**

SIR:

In order to comply with discretionary regulations 37 CFR 1.97 and 1.98, attached hereto is a copy of Form PTO-1449. Copies of such documents were made of record in the parent case, U.S. Patent Application No.: 10/151,416 filed May 20, 2002. These documents contain information which the Examiner may consider to be important in deciding whether to issue a patent in the instant application.

As this Statement is being filed prior to issuance of a first Office Action, no fee is due.

The present Disclosure Statement is being submitted in compliance with 37 CFR 1.56 as an Examiner might consider any cited document important in deciding whether to allow the application to issue as a patent, but the citation of each document is not to be construed as an admission that such document is necessarily relevant or prior art. No representation is intended

that the cited documents represent the results of a complete search, and it is anticipated that the Examiner in the normal course of examination, will make an independent search and will determine the best prior art consistent with 37 CFR 1.104 (a), and in the course of such search will review for relevance every document cited on the attached form even if not initialed.

Early and favorable consideration is respectfully solicited.

Any fee due with this paper may be charged on Deposit Account 50-1290.

Respectfully submitted,

Michael I. Markowitz Reg. No. 30,659

Customer Number: 026304

Docket No: NEC 19.740A

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1449/PTO			U.S. Dep	Application No. :					
Patent and Trademark Office						Filing Date :			
INFORMA	DIS	SCLOSURE		First Named Inventor: N. KIMURA					
STATEMENT BY APPLICANT						Group Art Unit :			
Sheet 1	of 1					Examiner Name :			
						Attorney Docket No.: NEC 19.740A			
U.S. PATENT DOCUMENTS									
Examiner	Cite	е	U.S. Patent	Kind	Name of Patentee or Applicant			Date of	Pages, Columns Lines
Initials	No.		Document	Code	of Cited Document			Publication of	of Where Relevant
				if known <sup>2</sup>				Cited	Passages or Relevant
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Other Prior Art-Non Patent Literature Documents  Examiner   Cite   Include name of the author (in CAPITAL LETTERS), title of the article (when   Applicant check her if English									
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<sup>1</sup>Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that Issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.1<sup>6</sup> if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.Burden Hour Statement: This form is estimated to take .2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of Information unless it displays a valid OMB control number. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.